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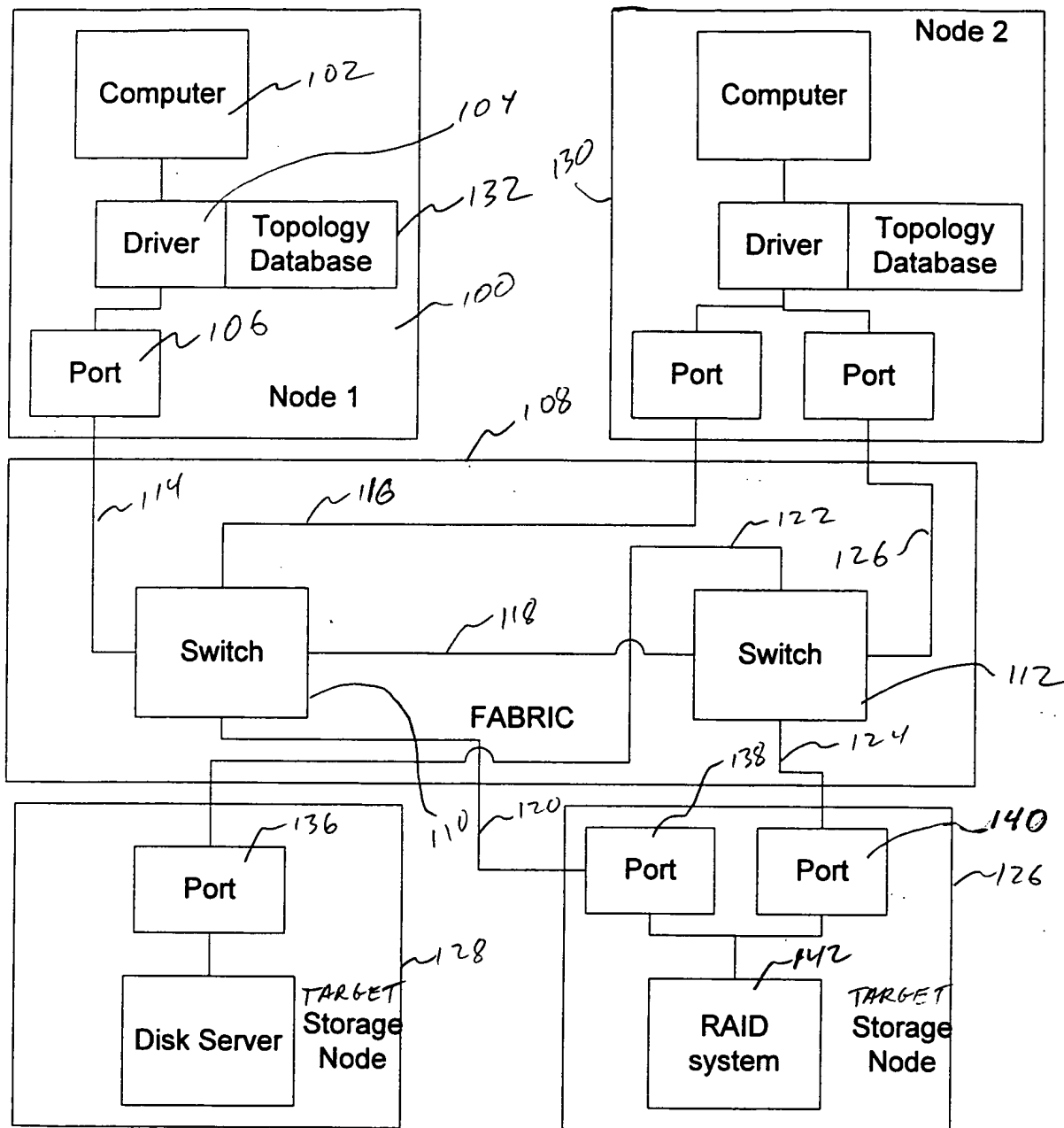


Figure 1

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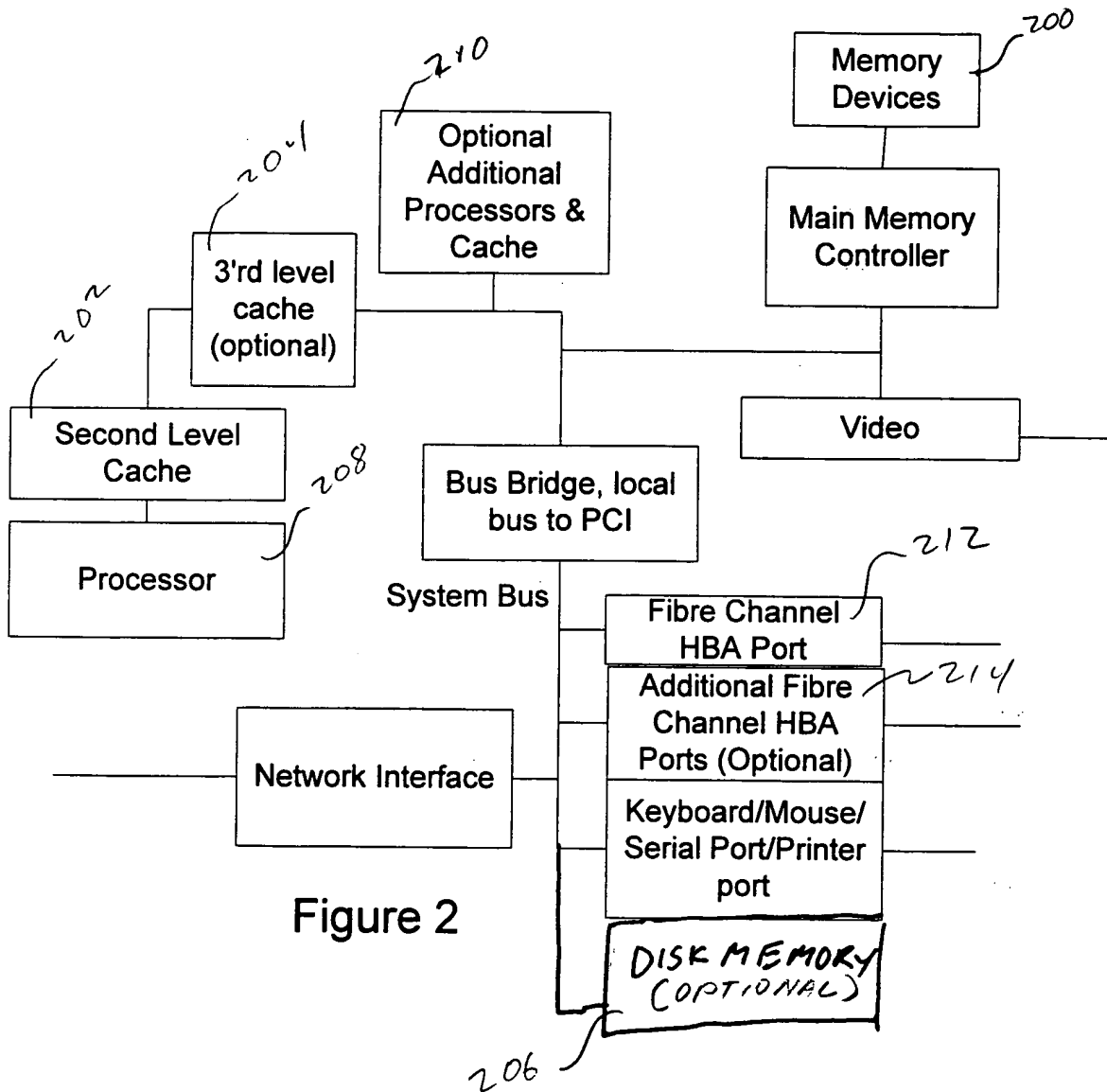


Figure 2

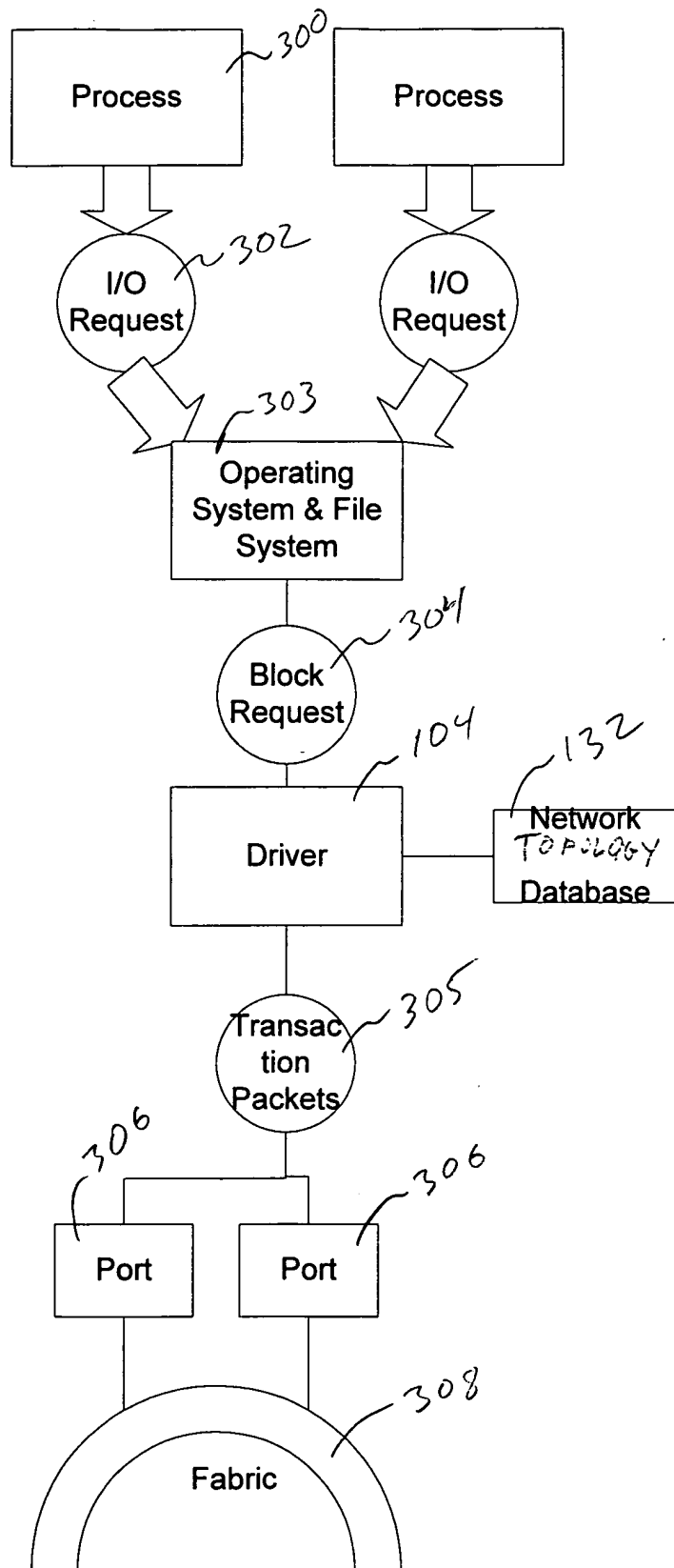


Figure 3

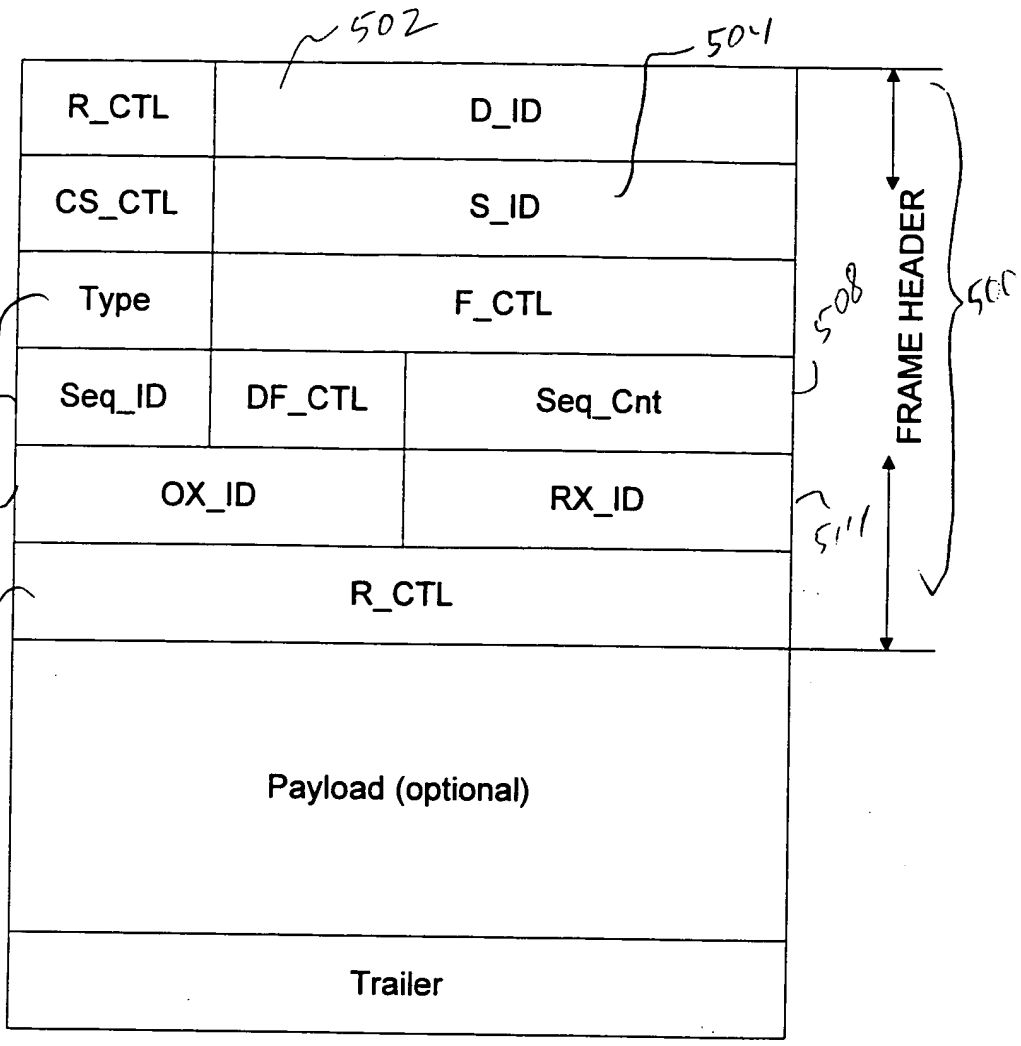
The diagram illustrates a storage system architecture with the following components and connections:

- HBA Port List (400):** Contains **Head & Tail Ptrs (402)**, **HBA Port (404)**, and **HBA Port (406)**. Arrows point from the HBA ports to the Node-Link Lists.
- Node-Link Lists:** Contains **Node Link** blocks. Arrows point from the HBA ports to these lists. A **Node Link (416)** is also shown with a feedback arrow to the HBA Port List.
- Target Node List (417):** Contains **Head & Tail Ptrs (418)** and **Node Record (414)** blocks. Arrows point from the Node-Link Lists to these records.
- Device-Link Lists:** Contains **Device Link** blocks. Arrows point from the Node Records to these lists.
- Device List:** Contains **Head & Tail Ptrs (436)** and **Device Record (432)** blocks. Arrows point from the Device-Link Lists to these records.
- Path Link & Status List:** Contains **Path Link & Stat** blocks. Arrows point from the Device Records to these blocks.

Handwritten annotations include: 402, 404, 406, 400, 416, 418, 414, 417, 422, 424, 426, 436, 432, 434, 440, 442, 438, 444, 446, 448.

Figure 4

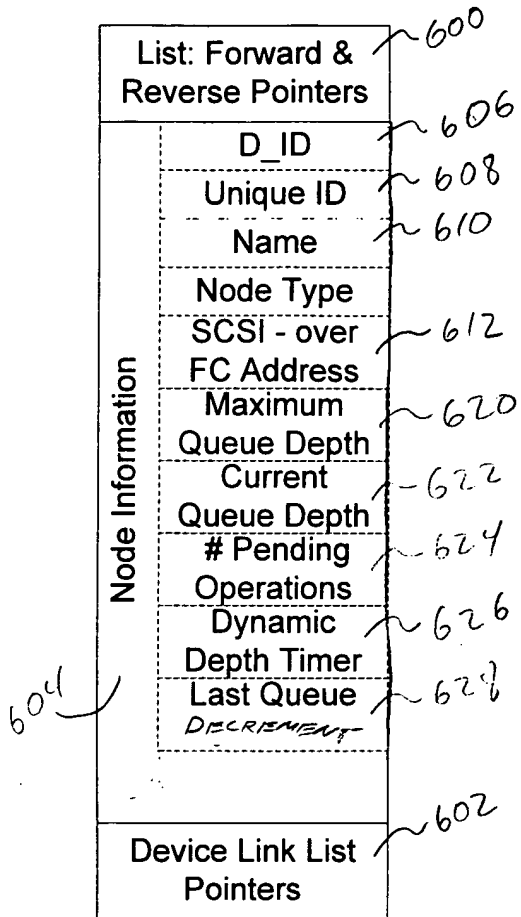
00T280" 52324960



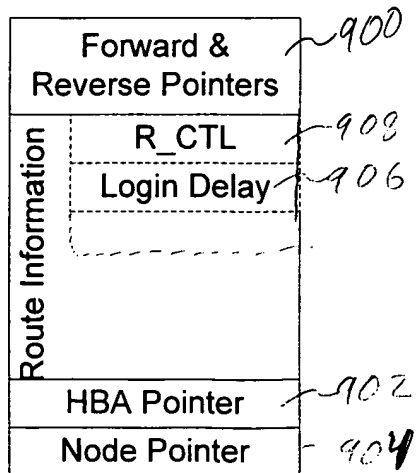
Frame Structure

KNOWN ART

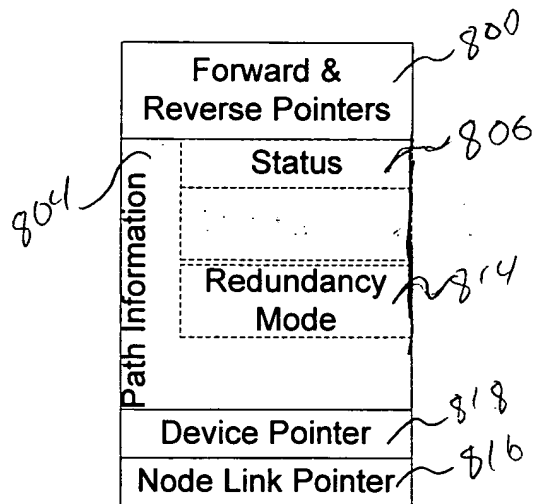
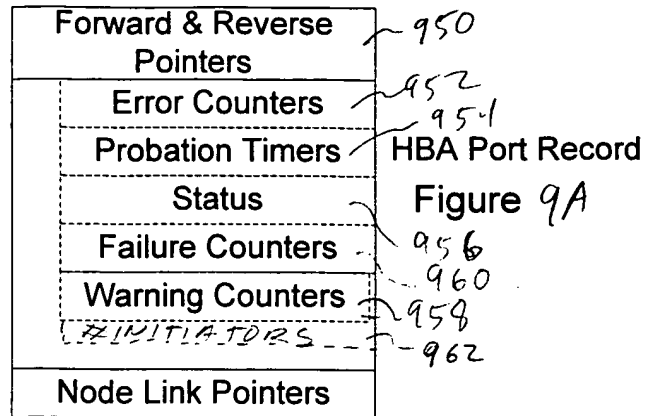
Figure 5



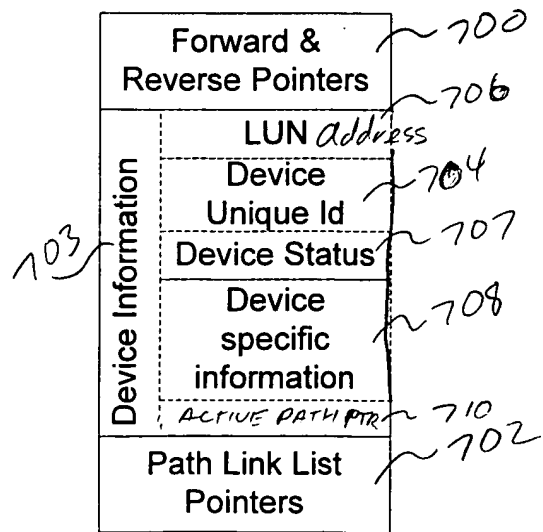
Node Record Figure 6



Node Link Figure 9



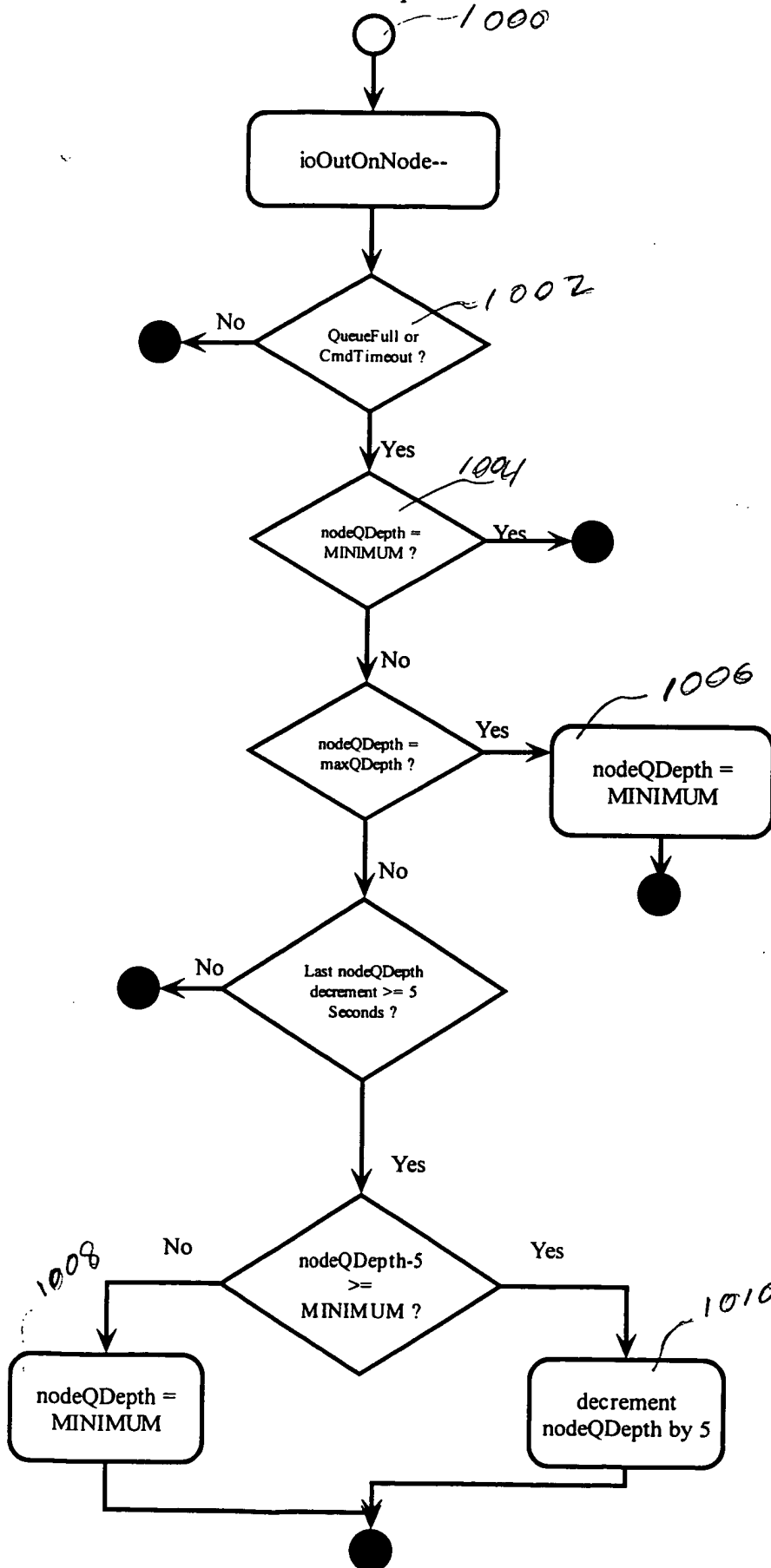
Path Link Figure 8



Device Record

Figure 7

I/O Completion



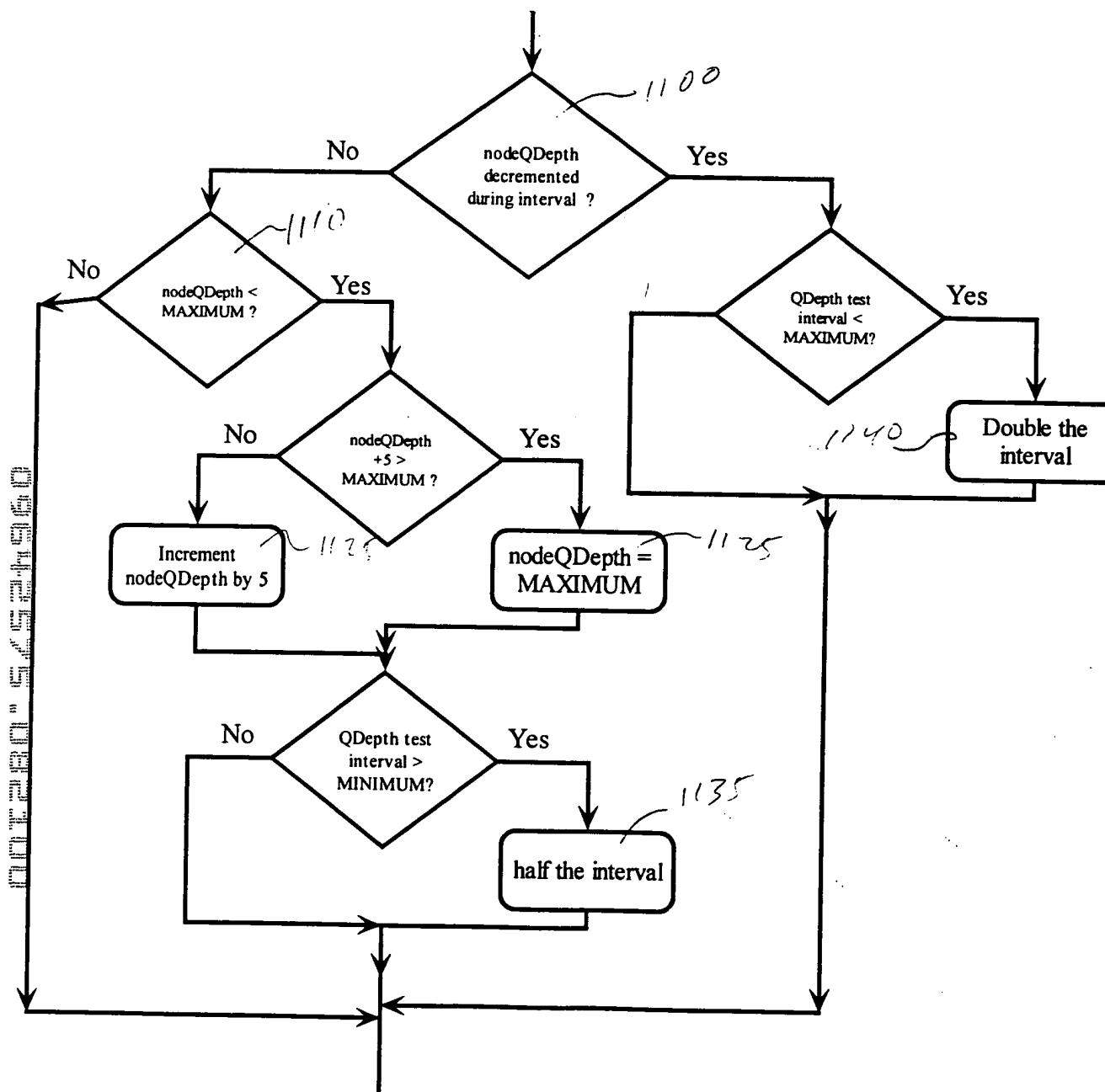


Figure 11

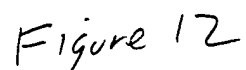
[illegible]

Figure 12

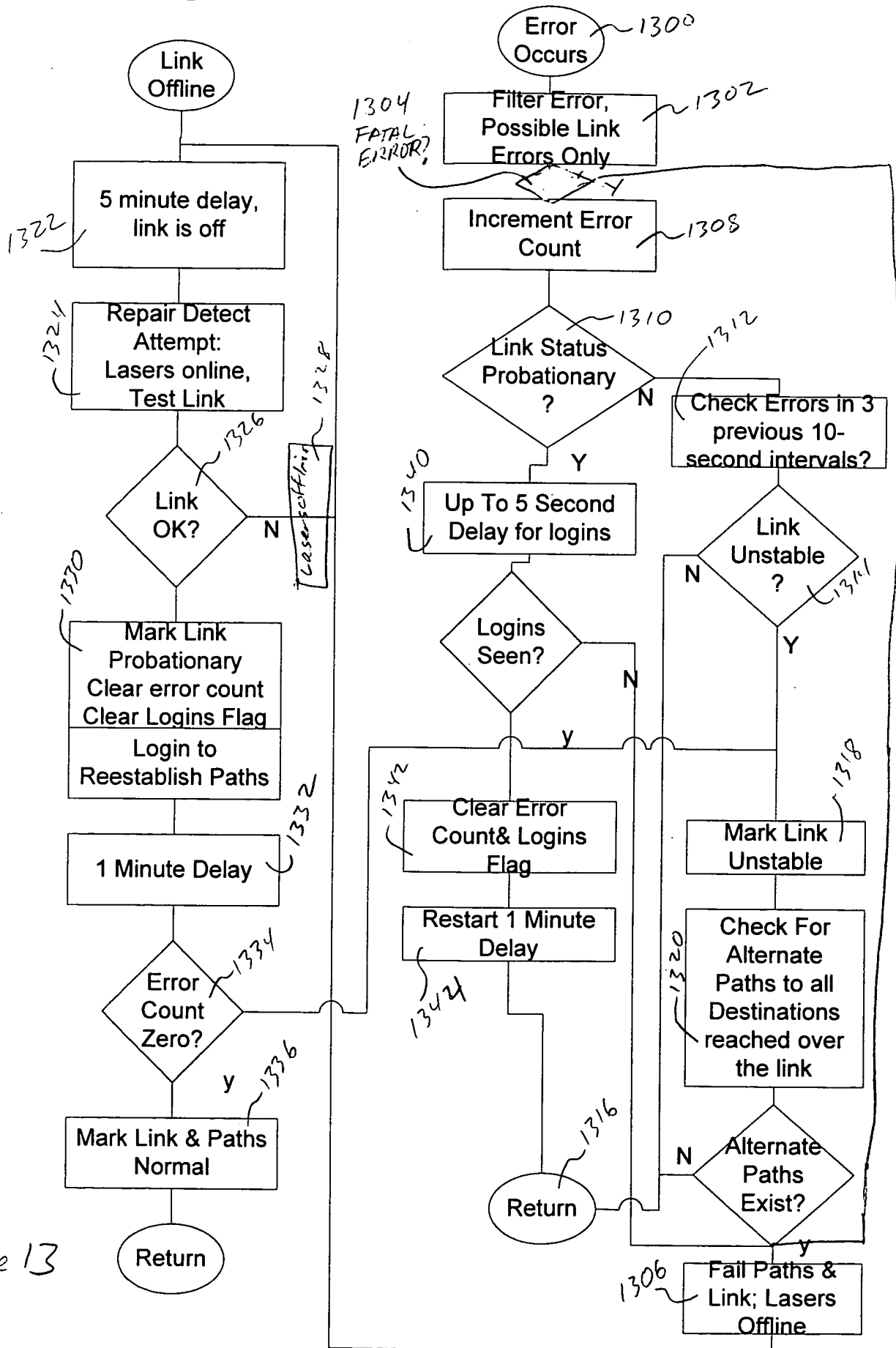
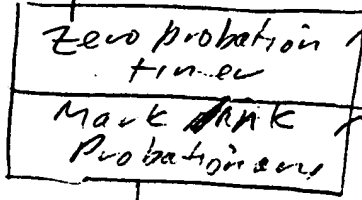


Figure 13

REPAIR
Detected



1400

1402

Figure 14

```

graph TD
    Start([Exchange Start]) --> D1{active path pin valid?}
    D1 -- Y --> P1[ ]
    D1 -- N --> D2{path active?}
    D2 -- Y --> P1
    D2 -- N --> D3{path normal?}
    D3 -- Y --> P1
    D3 -- N --> D4{path probationary?}
    D4 -- Y --> P2[ ]
    D4 -- N --> D5{was any path taken off line as unstable or failed?}
    P1 --> P2
    P2 --> P3[ ]
    P3 --> P4[ ]
    P4 --> P5[ ]
    P5 --> P6[ ]
    P6 --> P7[ ]
    P7 --> P8[ ]
    P8 --> P9[ ]
    P9 --> P10[ ]
    P10 --> P11[ ]
    P11 --> P12[ ]
    P12 --> P13[ ]
    P13 --> P14[ ]
    P14 --> P15[ ]
    P15 --> P16[ ]
    P16 --> P17[ ]
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```

Figure 15